

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,900,549 B2
APPLICATION NO. : 09/760741
DATED : May 31, 2005
INVENTOR(S) : Jerry M. Brooks

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Specification, the following errors are corrected:

Column 1, line 31, "it Limits" should read --it limits--; and

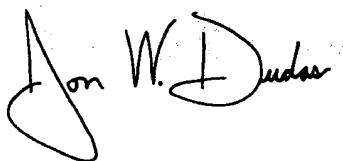
Column 4, lines 17-18, "above. It" should read --above. As shown in Figure 7, when the adhesive fillet 24_b is eliminated, there can be cavities 25 between each of the second and third semiconductor dies 30, 45 and between the first semiconductor die 20 and the supporting structure 10. It--.

In Claim 9, the following is corrected:

Column 5, line 39, "arid" should read --and--.

Signed and Sealed this

Thirteenth Day of March, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office